

**AMENDMENT TO THE CLAIMS**

**IN THE CLAIMS**

Please amend claims 1-7 as follows. A copy of all pending claims and a status of the claims is provided below.

1. (Currently Amended) A thin film transistor array panel<sub>1</sub> comprising:

an insulating substrate;

a gate line formed on the substrate;

a plurality of storage ~~conductors~~ electrodes formed on the substrate, each storage ~~conductor~~ electrode including a plurality of branches;

a gate insulating layer formed on the gate line and the storage ~~conductor~~ electrode;

a semiconductor layer formed on the gate insulating layer;

a data conductor formed on the data conductor layer; and

a pixel electrode layer formed on the passivation layer<sub>1</sub>

wherein at most one of the branches of each storage ~~conductor~~ electrode has an isolated end.

2. (Currently Amended) The thin film transistor array panel of claim 1, wherein ~~adjacent two of the storage conductors have at least two connections~~ longitudinal portions of adjacent storage electrodes are connected by connecting portions.

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3. (Currently Amended) The thin film transistor array panel of claim 1, further comprising a connection bridge having a portion thereof connected to each of the isolated ends of the plurality of storage conductors and a portion thereof connected to a storage electrode line formed on the substrate ~~adjacent two of the storage conductors across the gate line~~ .

4. (Currently Amended) The thin film transistor array panel of claim 1, wherein each storage conductor further comprises two longitudinal branches and two oblique branches, and the branches of each storage conductor form a closed loop.

5. (Currently Amended) The thin film transistor array panel of claim 1, wherein each storage conductor comprises two longitudinal branches connected to ~~and~~ three oblique branches, ~~and the branches of each storage conductor~~ the connected branches forming two closed loops.

6. (Currently Amended) The thin film transistor array panel of claim 1, wherein each storage conductor comprises two longitudinal branches connected to ~~and~~ four oblique branches, ~~and the branches of each storage conductor~~ the connected branches forming three closed loops.

7. (Currently Amended) The thin film transistor array panel of claim 1, wherein the pixel electrode has a plurality of cutouts, and at least one of the cutouts overlaps the storage ~~conductors~~ electrode.

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8. (Original) The thin film transistor panel of claim 1, wherein the data conductor has substantially the same planar shape as the semiconductor layer except for a channel portion of the semiconductor layer.